

Oxide Coatings for Metallic SOFC Interconnects

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Cost Effective Interconnect Coating (IC) Process Development

- Phase I: Aerosol spray deposition (ASD) demonstrated as a commercially-viable process
- Phase II: Process Refinement and Validation

- 1. Project Objectives and Conclusions**
- 2. Summary of cost modeling**
- 3. Overview of coating evaluation methodology**
- 4. Performance evaluation results**
 - Oxidation kinetics
 - Long-term ASR results
 - Preliminary mechanical testing (collaboration with OSU)
 - Cr vaporization testing (collaboration with UConn)
 - Continuous process improvements
- 5. Extension of coating process to adjacent markets**

Project Objectives

Track I (Cost Modeling): Develop and production-validate cost models for ASD coating at various production volumes.

- ❖ Develop customer-specific cost curves.
- ❖ Develop Customer-preferred paths to market
- ❖ Identify and address customer specific technical hurdles.
- ❖ Identify manufacturing strategies to reduce volume manufacturing costs.

Track II (Performance Validation): Demonstrate ASD-coated ICs performance to reinforce value proposition.

- ❖ Identify test methods to simulate 40,000 hours service.
- ❖ Develop model for IC degradation.
- ❖ Based on models, identify cost and performance optimized coatings.
- ❖ Evaluate performance of ASD coated components.

Conclusions

- ❖ **Refined cost and manufacturing models to encompass volumes from prototyping through full volume production.**
 - Market forecast and demand curves defined for three OEM profiles at various stages of commercialization.
 - Three-stage technology roadmap developed.
- ❖ **Identified manufacturing strategies to reduce volume manufacturing costs.**
 - Materials processing scale-up to 25 kg batch sizes and beyond
- ❖ **Defined key process limits for ASD coated ICs.**
 - Lifetime stability tests in progress (> 1800 hrs operation at 800 °C in both single and dual atmosphere configurations).
- ❖ **Identified key failure mechanisms and acceleration factors.**
 - Predictive lifetime models successfully applied to long-term stability tests.

NEXTECH MATERIALS

Invent, Develop, Deliver

***Cleaning
System
(incoming
substrates)***



***Controlled Atmosphere
Electric Pusher Kiln***



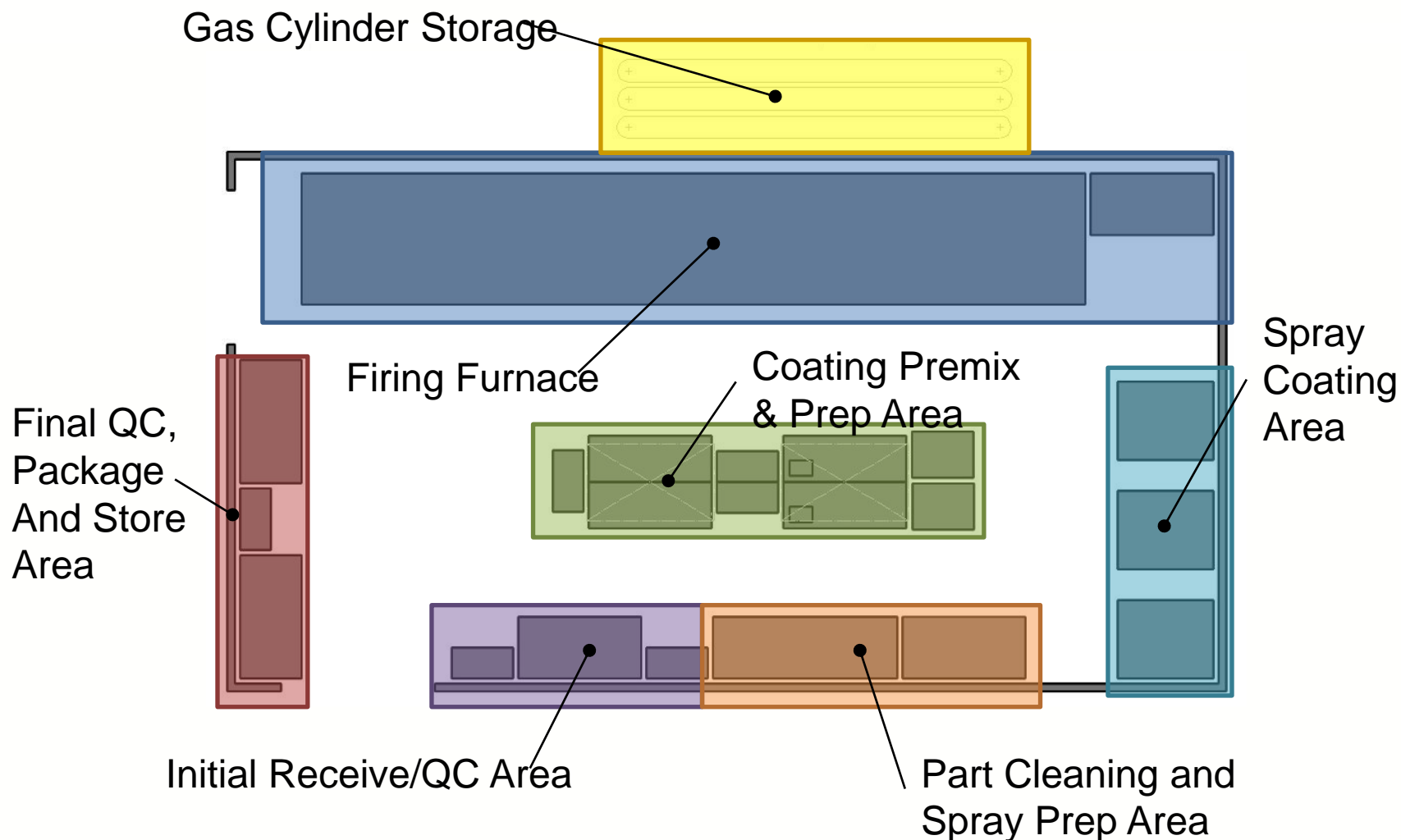
Integrated Spray/Dry System



***Automated QA/QC
Inspection Equipment***



Production Plant Layout (35 x 65' facility)



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Validation testing methodology

1.1

1.2

1.3

1.4

1.5

1.6

1.7

1.8

1.9

1.10

1.11

1.12

1.13

1.14

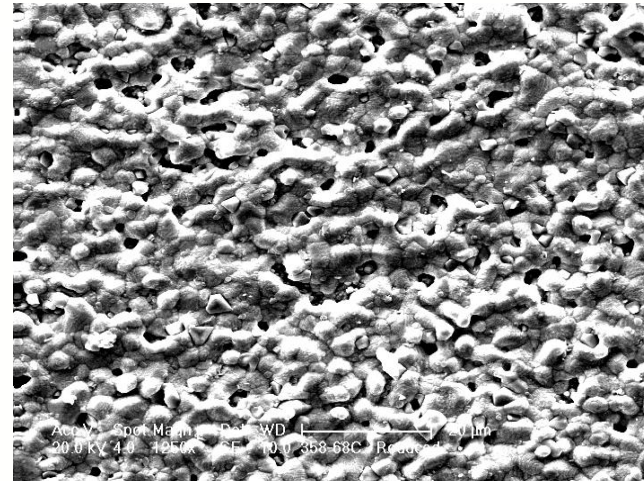
Initial testing

Preliminary
coating quality
assessment

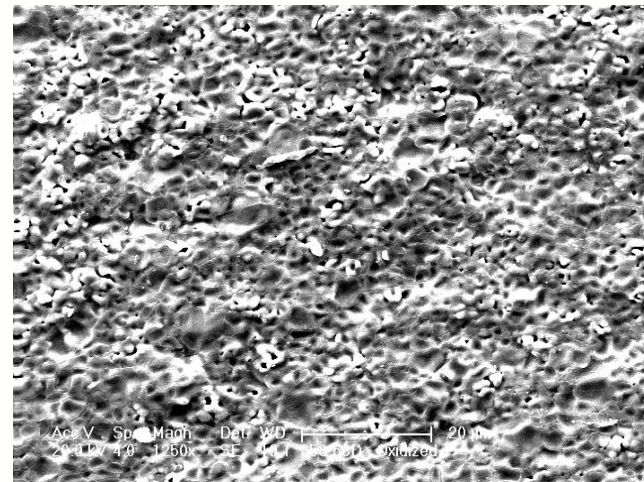
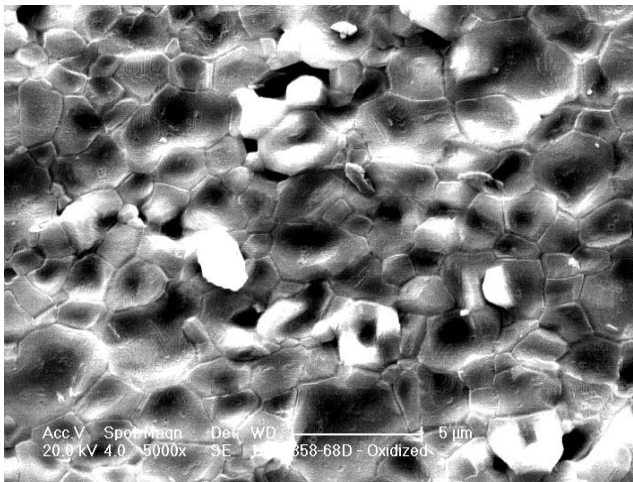
Integrated
Interconnect
coating
lifetime model

MCO Coating Morphology

Reduced



Oxidized



Validation testing methodology

Initial testing Accelerated testing Long term testing

Initial testing

Preliminary
coating quality
assessment

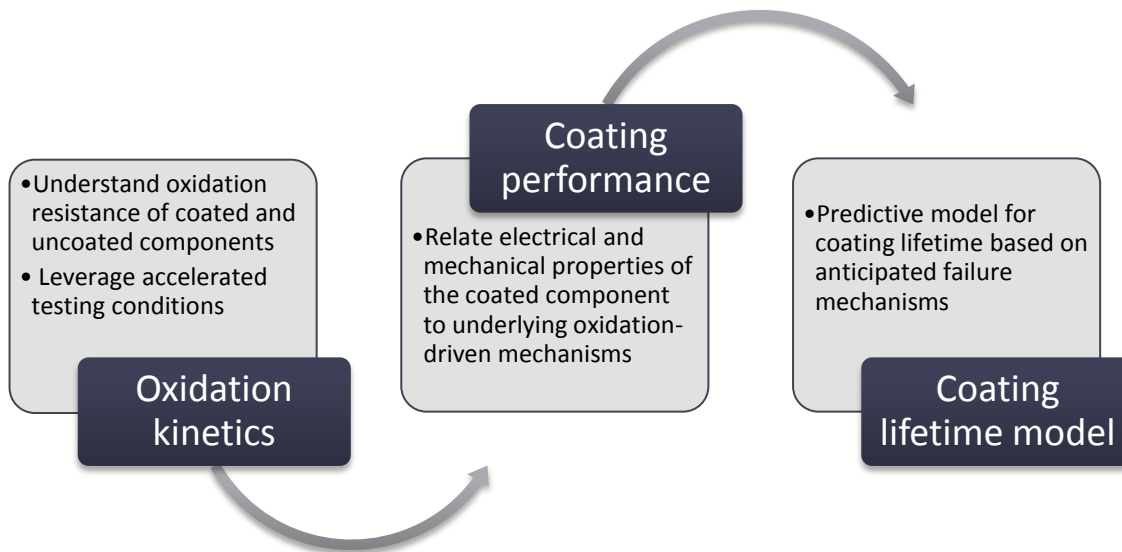
Oxidation
Kinetics

Coated
Interconnect
Failure Analysis

Integrated
Interconnect
coating
lifetime model

Oxidation testing

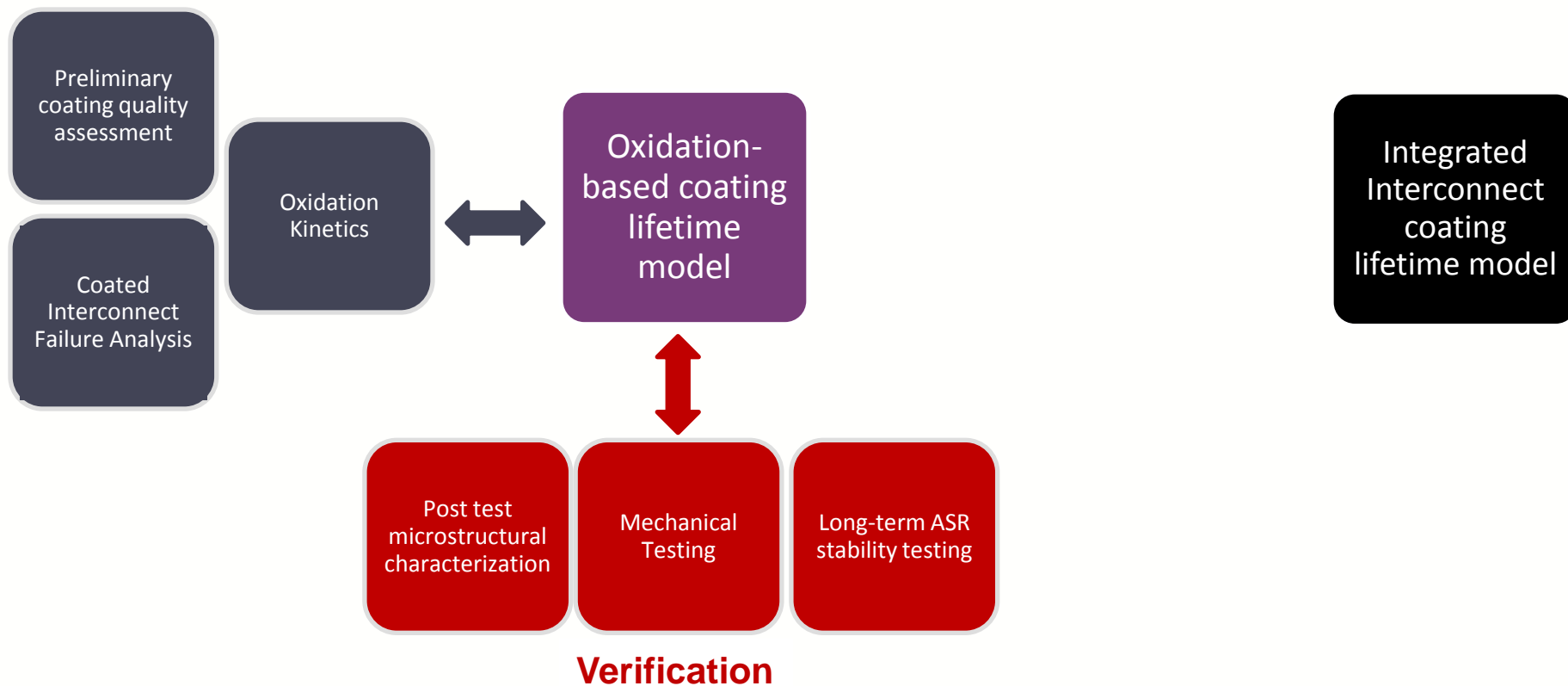
- ❖ Initial coating performance influenced by a range of factors (substrate, coating)
- ❖ Oxidation driven failure mechanisms identified as most likely limiter of component life
- ❖ Oxidation-based model for long-term coating behavior.



*10 μ m MCO coating on SS441 substrate
900 °C, 200 hours, Air*

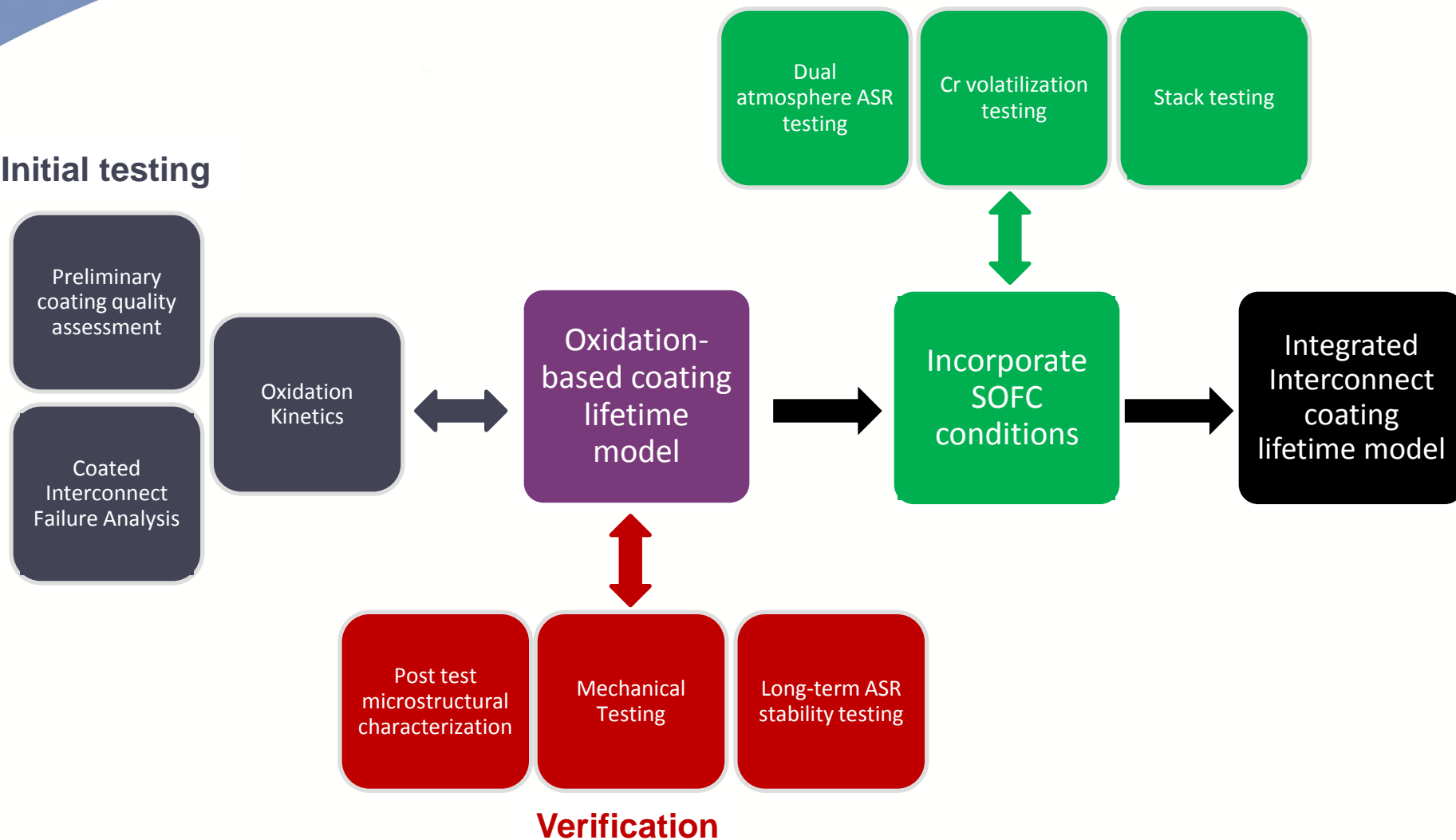
Validation testing methodology

Initial testing



Validation testing methodology

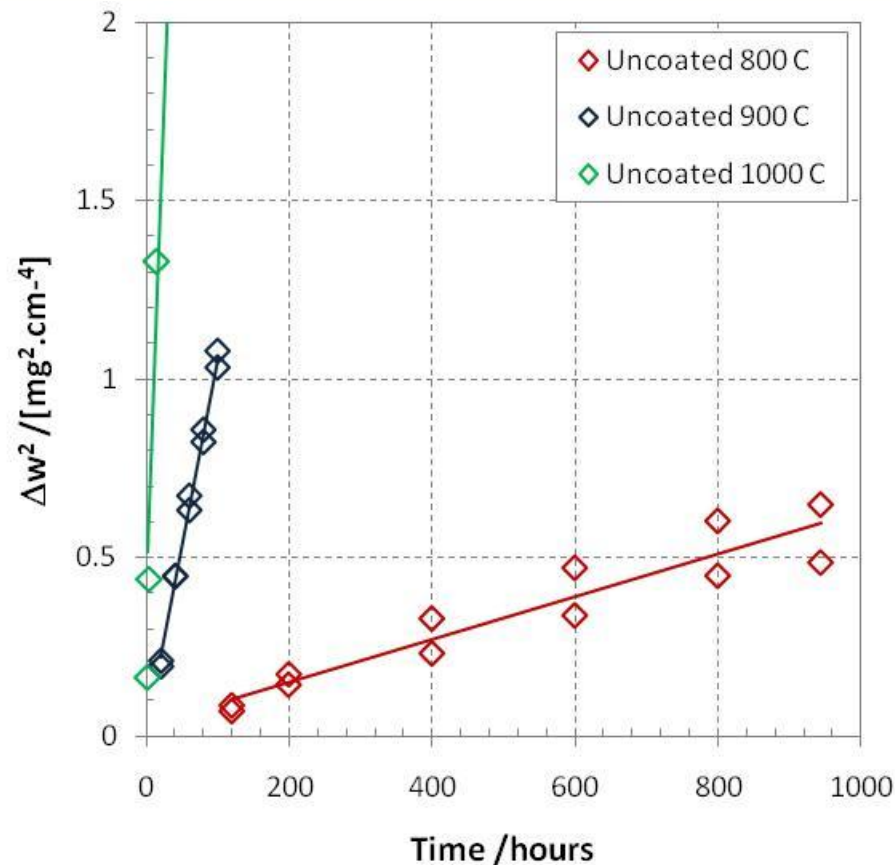
Initial testing



Oxidation experiments

- ❖ Investigate oxidation kinetics for uncoated and MCO coated SS441 (800 -1000 °C).
- ❖ Uncoated samples show accelerated oxidation kinetics with increasing temperature

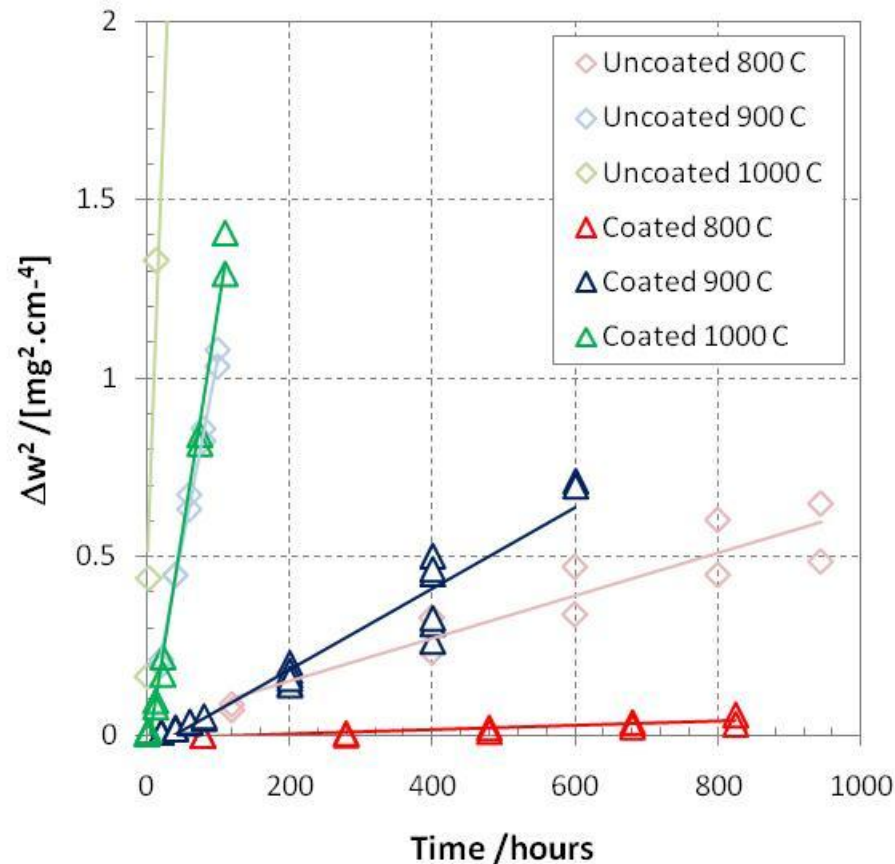
Weight change vs. time for uncoated SS441



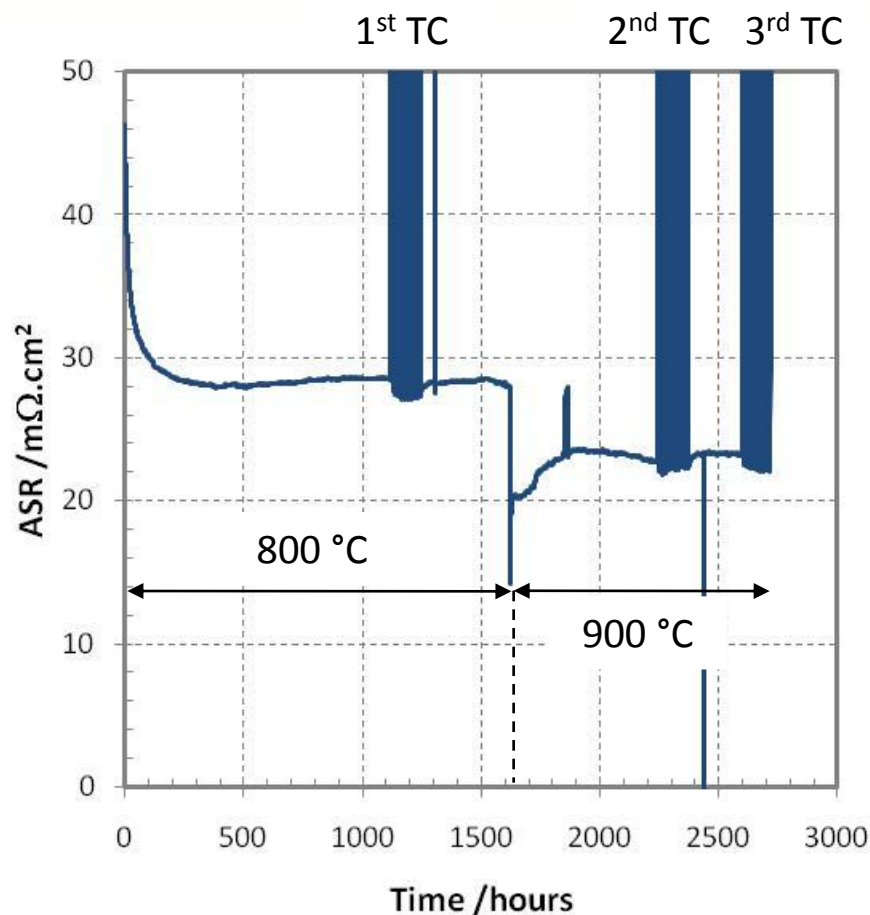
Oxidation experiments

- ❖ MCO coating significantly improves oxidation resistance at all temperatures (~ 10 X reduction vs. uncoated samples).
- ❖ Similar T dependence for coated samples: oxidation kinetics increases ~ 20X [800 → 900 °C]

Weight change vs. time for uncoated SS441



Symmetric ASR behavior

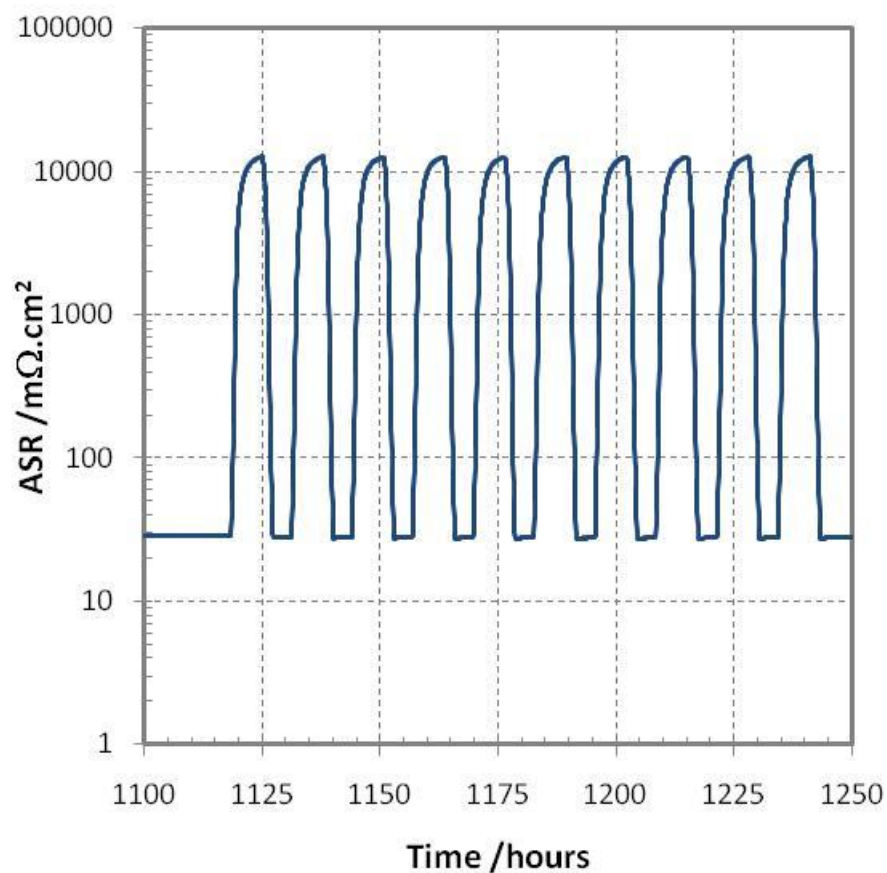


*Symmetrically MCO coated SS441
Humidified air, 800 °C / 900 °C
Current Density 0.5 A.cm²*

- ❖ Multiple samples running long-term stability tests

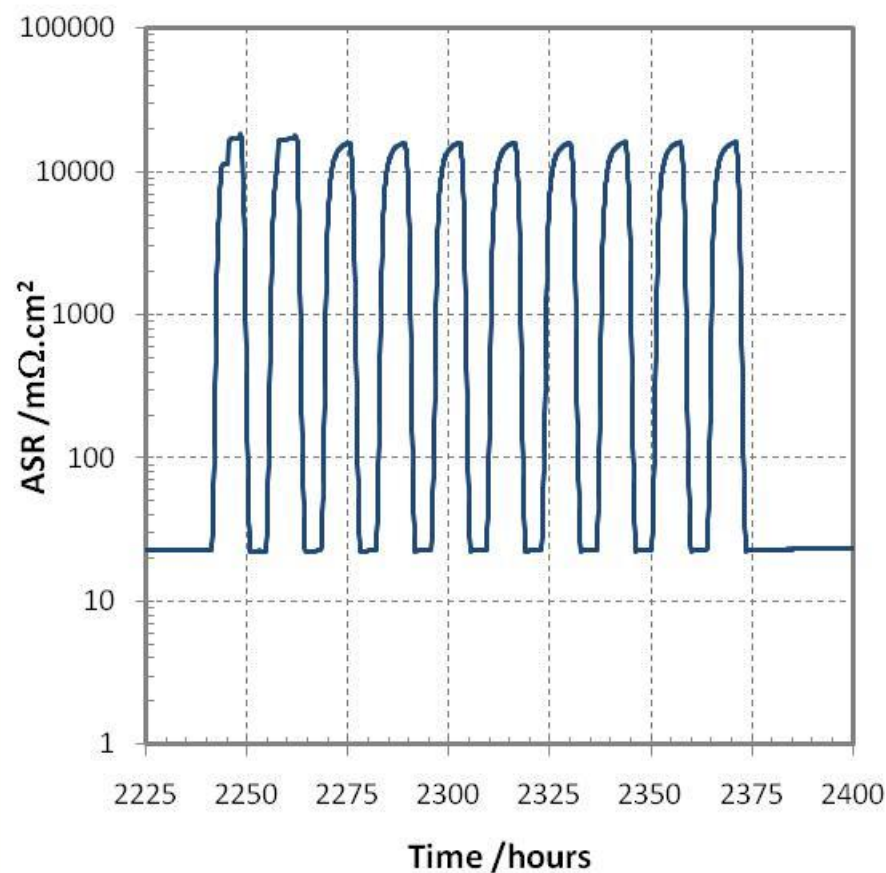
Thermal Cycling

10X thermal cycling 800 °C



Symmetrically MCO coated SS441
Humidified air, 800 °C to 50 °C, 0.5 A.cm⁻²

3rd 10X thermal cycling 900 °C

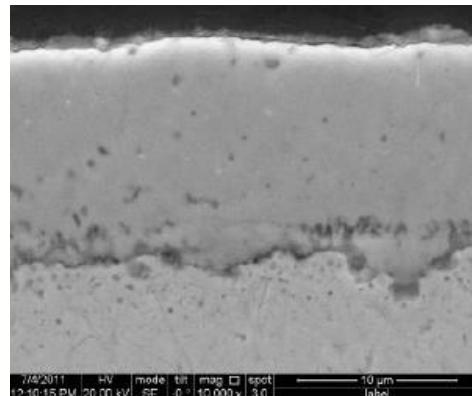
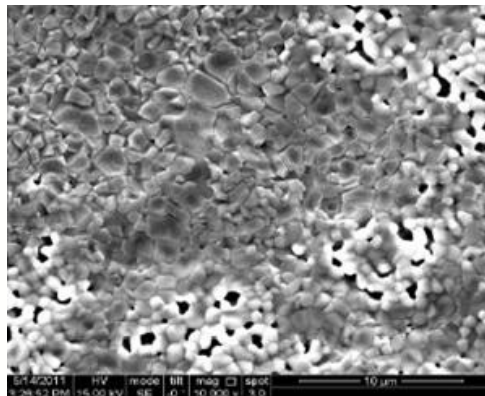


Symmetrically MCO coated SS441
Humidified air, 900 °C to 50 °C, 0.5 A.cm⁻²

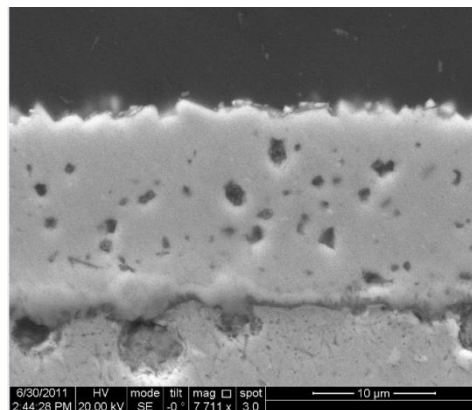
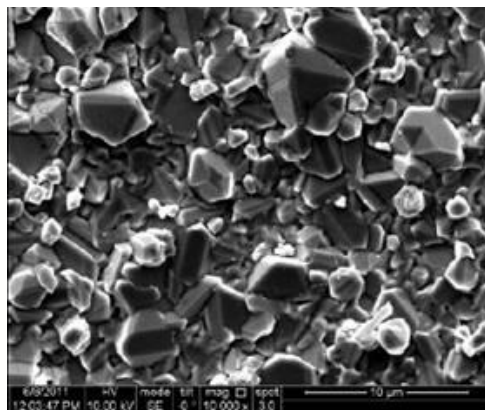
Cr vaporization characterization

- ❖ Collaborating with Dr. Singh at the University of Connecticut to evaluate the effectiveness of the MCO coating at preventing Cr vaporization.

As received



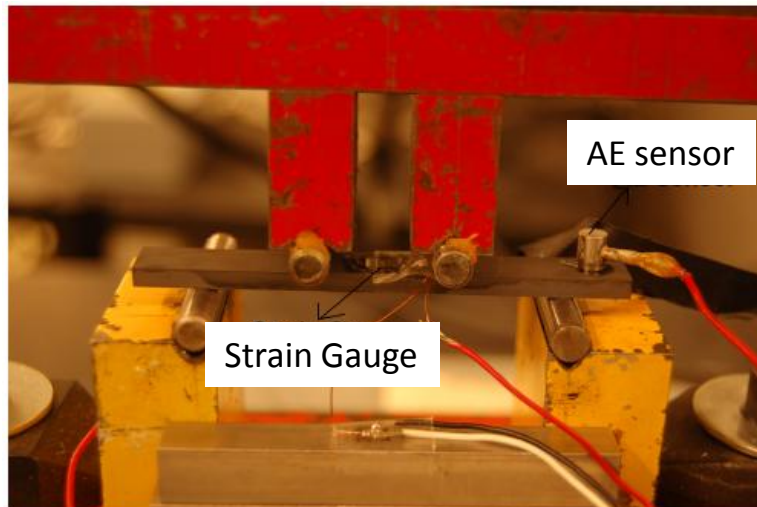
After oxidation
850 °C, 500 hrs
90 % O₂–10 %
N₂ 3% H₂O)



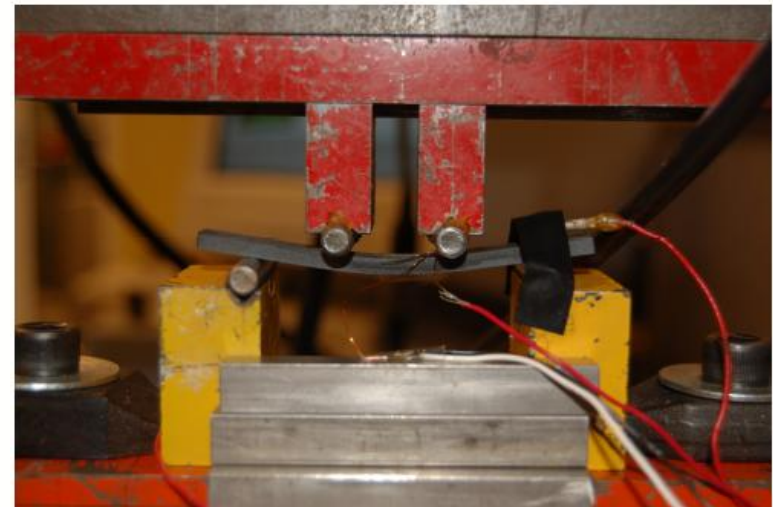
Mechanical Testing

- ❖ Collaborating with Dr. Mark Walter's group at OSU through NSF, GOALI program.
- ❖ Investigating interfacial strength of our coating on a range of substrates through synchronized four-point bend and acoustic emission testing

Four-point bend set-up



Coated IC bar undergoing test

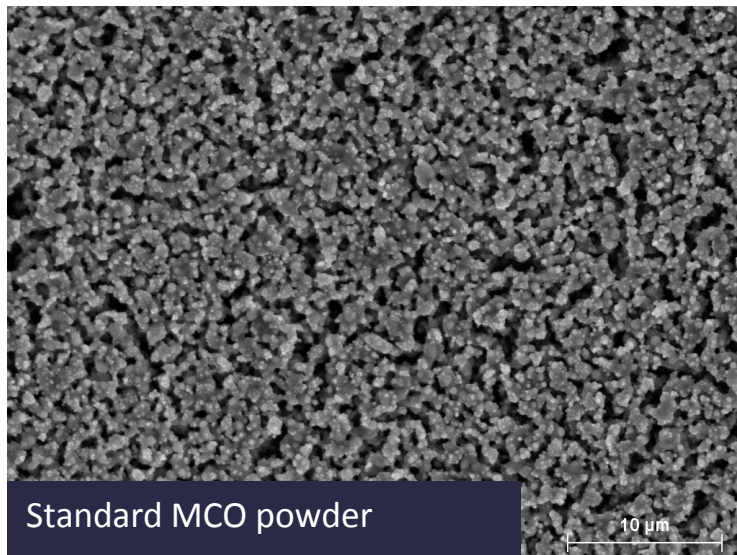


- ❖ Investigating how interfacial strength changes with time to develop lifetime prediction for IC coating – enable design of optimized coating/substrate solutions.

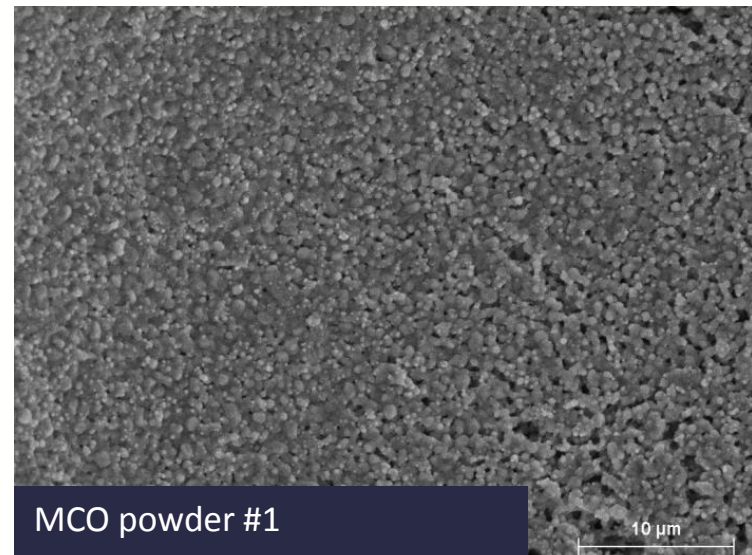
Continuous Coating Improvements

- ❖ Process improvements are focused on enhancing the performance of the coating and also improving the process capability.
- ❖ Example below is how modification of the source MCO powder is enabling improved coating quality

Baseline coating



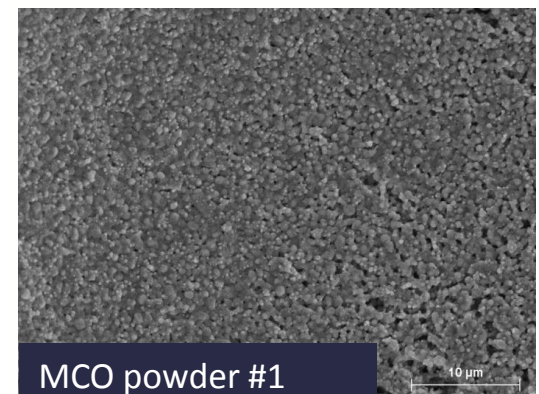
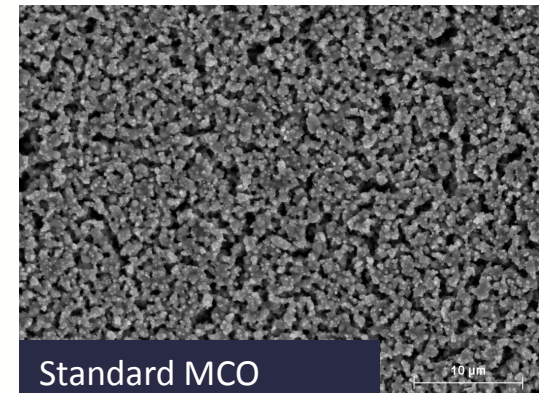
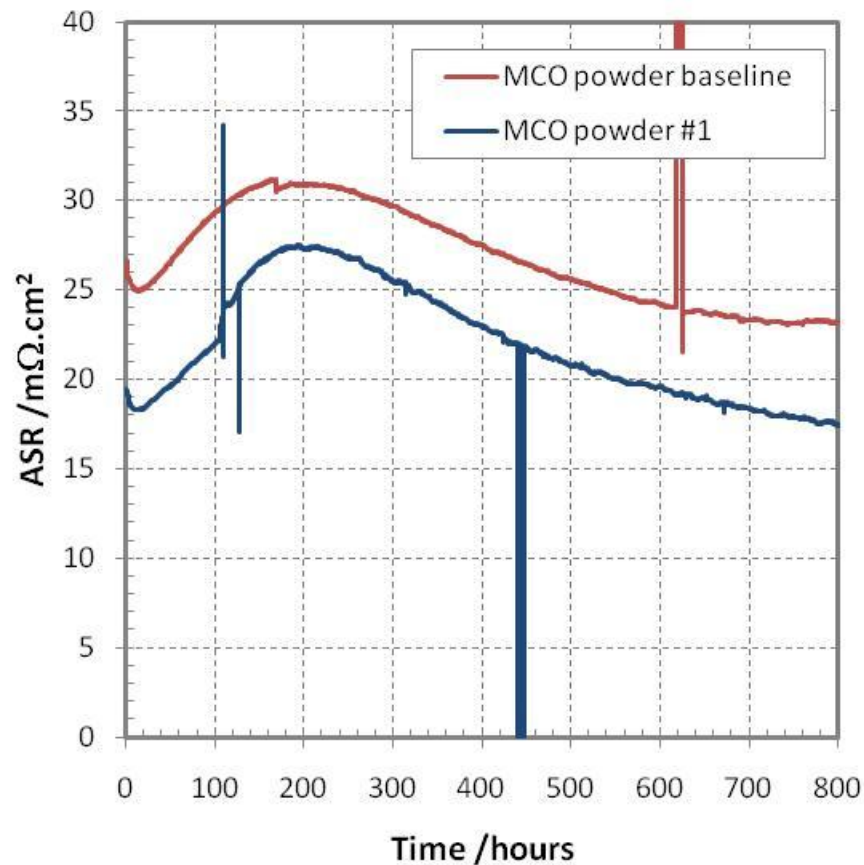
Coating with MCO powder improvement



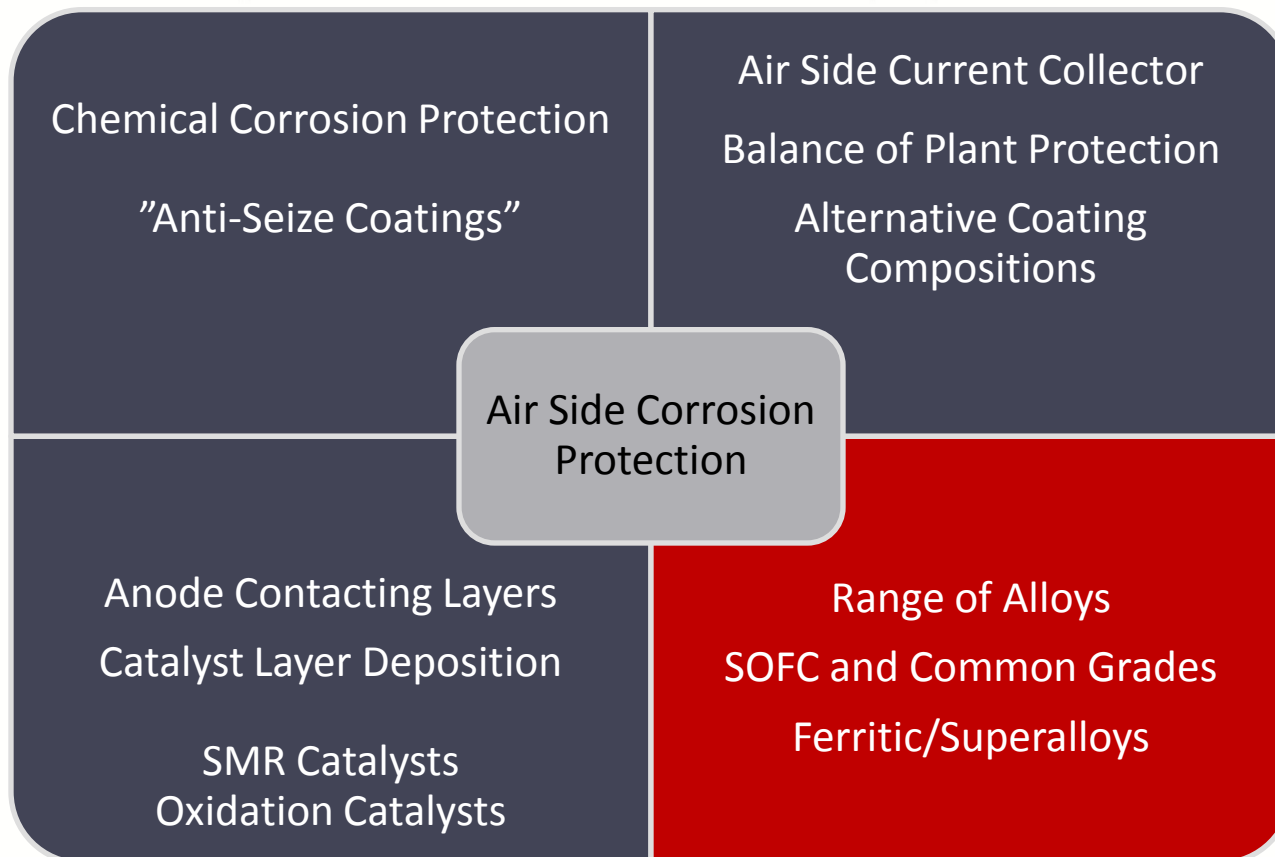
Top-down SEM of MCO coating in reduced state

Continuous Coating Improvements

ASR vs. time for improved MCO powder coating

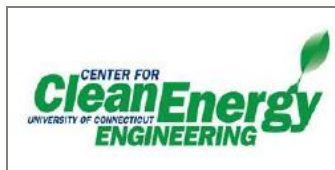


Process Compatibility



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 - Briggs White, Project Manager
- State of Ohio Third Frontier Program



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